

## ABSTRACT OF THE DISCLOSURE

### APPARATUS AND METHOD

#### FOR INTEGRATED CHIPSET CONTENT PROTECTION

The inferred hardware assisted decryption apparatus and method utilizes a re-configurable hardware block in conjunction with a processor running a software decryption algorithm that determines the form of the hardware. The re-configurable feature allows the hardware to be changed at regular intervals, thus circumventing any attempts at compromising the hardware. In particular, an encrypted code defining a unique hardware configuration is decrypted based upon a stored key. A unique hardware configuration is then established based upon the encrypted code. A decryption operation is then performed on encrypted content information utilizing the unique hardware configuration.

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